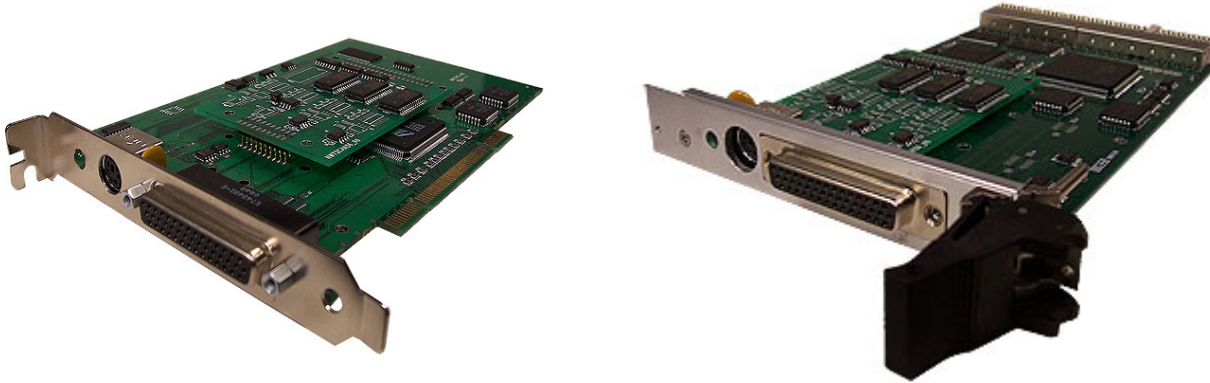


NETWAY 72 PCI / PXI Data Sheet

Real-time multiplex simulation and analysis tool



FEATURE	NETWAY 72 PCI/PXI
Analog / Digital Inputs	8, optionally can be extended to 12
Digital Outputs	7, optionally can be extended to 15
PWM Inputs / Outputs	2 PWM Inputs / 4 PWM Outputs
(*)CAN channels	Maximum 10 CAN channels, selectable transceivers HSC, FTC, SWC
(*)UART channels	Maximum 10 UART channels, transceivers K-Line, RS485, RS232
(*)J1850 PWM	1 channel
(*)J1850 VPW	1 channel
(*)I2C channel	1 channel
(*)SPI channel	1 channel
PC Interface	Mini USB (switchable to serial for firmware updates)
Network Connector	44-pin HDB connector for power, networks, and I/O signals
Dimensions	Single-slot PCI / PXI card
Application compatibility	Windows XP and later (32 or 64 bit OS)
Control Library	nwCtrl.dll – MS Studio 6, LabWindows, LabView (32 or 64 bit OS) nwCtrlCOM.dll – MS Studio 8, MS Studio 10 (32 or 64 bit OS)
Product ID	NW72PCI or NW72PXI

Note 1: Items designated with (*) are optional, specified by a customer

Note 2: Abbreviations: **HS** – high-speed CAN transceiver up to 1 Mbits /sec, **FT** – fault-tolerant CAN transceiver 83.3 Kbits/sec, **SW** – single-wire CAN (GMLAN) – 33.3 Kbits/sec

Note 3: First two CAN and UART channels have dedicated pin-out assignments, the remaining 8 channels can be alternatively either CAN or UART pluggable daughterboard, therefore pinout depends on which daughterboard is selected. A maximum of eight additional CAN/UART daughter boards/channels are available.

Note 4: Control Library (nwCtrl.dll and nwCtrlCOM.dll) license must be purchased separately. The department license comes with a library manual and demo examples

Tool Features:

- **CAN:** up to ten (10) independent CAN channels
 - Selection of transceivers per channel: High-Speed Dual-Wire / Fault-Tolerance / Single-Wire(GMLAN)
 - Standard 11-bit and extended 29-bit header
 - Supports multi-frame CAN (ISO-15765)
 - Supports J2534 with SET32.DLL library
 - J1939, J1979, and more
- **UART:** up to ten (10) independent channels: UART protocols - LIN 2.1(1.3), KWP2000, KWP1281, ISO9141-1, ISO9141-2, J1708, J1587, SAEJ1922, etc. Selection of transceivers per channel: LIN (K-Line), RS485, RS232, AOS.
- **J1850:** two independent channels VPW (Class2) and PWM (SCP)
- **IIC:** one channel up to 400 Kbits/sec, Master or Slave configuration
- **SPI:** one channel up to 10 Mbits/sec, Master or Slave configuration
- **I/O Outputs** are open drain, pull-up maximum to 50V, load maximum 350mA.
- **I/O Input** signals range 0~15V, 10 bits resolution.
- **Emulation** script language for real-time networks and signals simulation, worst-case resolution less than 1ms for all operations including data logging, network simulations, and I/O signal measurement and generation.
- **Events** triggered by network messages, timers, or/and I/O signal transitions
- **Stand-alone** operations including in-vehicle gateway, end-of-line test, on bench rest of vehicle simulator, data logging, etc.
- Internal circular buffer memory capacity - up to 32,000 messages. Flexible dynamic filtering for data logging.
- Unlimited logging and analysis with a variety of graphical network signal representations are available. Traffic replay and more.
- **Warranty** and technical support (free software/firmware upgrades) for one full year from the purchase date.